

EAMTA SCHEDULE

	Sábado 9	Domingo 10	Lunes 11	Martes 12	Miércoles 13
9.00 a 11.00	Registración	ASADO	Clases/Labs	Clases/Labs	Clases/Labs
11.00 a 11.30			Coffee Break	Coffee Break	Coffee Break
11.30 a 13.00			Clases/Labs	Clases/Labs	Clases/Labs
13.00 a 14.30			Tiempo libre para almuerzo	Tiempo libre para almuerzo	Tiempo libre para almuerzo
14.30 a 16.00			Clases	Clases/Labs	Clases/Labs
16.00 a 16.30			Coffe Break	Coffee Break	Coffee Break
16.30 a 17.30			Plenaria/ Clases	"Redes Eléctricas Inteligentes" Patricio Donato	"From Lab to Fab" Adrián Faigón
17.30 a 19.00			Clases	Clases/Labs	Clases/Labs
19.00 a 21.00			Libre	Laboratorios	Laboratorios

CAE SCHEDULE

	Jueves 14	Viernes 15
9.00 a 10.00	Bienvenida Charla Allegro Daniel Musciano	Aplicación de procesamiento digital de señales en transceptores ópticos coherentes Ariel Pola, Inphi
10.00 a 10.30	Café	
10.30 a 11.30	Synopsys: EDA development for advanced Ics	SESION D
11:30 a 12:30	SESION A	SESION E
12:30 a 13:30	Sesión Posters + Empanadas	Sesión Posters + Empanadas
13.30 a 14.30	SESION B	SESION F
14.30 a 15.30	SESION C	"¿Qué es una aceleradora de tecnología? Caso CITES" Sergio Ritcher
15.30 a 16.00	Café	
16.00 a 17.00	"Inteligencia Artificial en tecnologías nanométricas" Pedro Julián	"El origen de las cosas" Mario Benedetti Cierre
17.00 a 18.00	Tutorial "Cómo encontrar el mejor mercado para mi tecnología basado en mis fortalezas técnicas." Sergio Ritcher	
20.30	Cena de camaradería Synopsys	-

CAE SESSIONS THURSDAY 14/3/2019

SESSION A - Thursday 14 - 11:30 to 12:30

A 10 nA integrated precision rectifier for implantable medical devices, Joel Gak, Matias Miguez, Alfredo Arnaud and Emilio Alvarez

Influence of Oscillator Topology on Fault Sensitivity in Oscillation Based Testing (OBT) of OTAs, Tinus Stander, Pablo Petrashin, Luis Toledo, Walter Lancioni, Carlos Vazquez and Fortunato Carlos Dualibe

Mimicking Spike-Timing-Dependent Plasticity with Emulated Memristors, Agustin Cisternas Ferri, Alan Rapoport, Pablo Ignacio Fierens and German Agustin Patterson

SESSION B - Thursday 14 - 13:30 to 14:30

High Gain Flatness Discrete Low Noise Amplifier for 3 to 5 GHz UWB operation, Pablo Gamez, Andres Altieri and Cecilia Galarza

Improved cordic angle computation for RF applications using an autorange circuit, Guillermo Jaquenod

Antenna Coupling and Out of Band Interference Effects on a High Precision GNSS Receiver, Ramón López La Valle, Javier García and Pedro Agustín Roncagliolo

Performance comparison of Precise Point Positioning using real-time oriented GNSS products, Ernesto Mauro López, Santiago Rodríguez, Javier Garcia and Carlos Muravchik

SESSION C - Thursday 14 - 14:30 to 15:30

Injection Measurements and Simulation for a Floating Gate MOSFET Designed for Radiation Measurements, Sebastián Carbonetto, Juan Cruz Suárez Martene, Mariano García Inza and Adrian Faigon

Peltier based Temperature Controller for MOS Dosimeter Characterization, Rafael Garcia Cozzi, Sebastián Carbonetto and Adrian Faigon

Novel time-domain CMOS temperature sensor for passive RFID Tag, Martín Di Federico and Paola Ceminari

CAE SESSIONS FRIDAY 15/3/2019

SESSION D - Friday 15 - 10:30 to 11:30

Memory based computation core for nonlinear neural operations, Martín Villemur, Gaspar Tognetti and Pedro Julian

Chaotic Compressed Sensing System for 16x sub-Nyquist Signal Reconstruction, Mariano L. Acosta, Maximiliano Antonelli and Luciana De Micco

A Robotic Grasping Method using ConvNets, Luis Avila, Elio Ogas, Guillermo Larregay and Daniel Moran

SESSION E - Friday 15 - 11:30 to 12:30

Optimal Filter Taking into Account the Charge Transfer Characteristic in CCD Readout, Pedro Querejeta Simbeni, Guillermo Fernandez Moroni, Fernando Chierchie, Miguel Sofo Haro, Angel Soto, Leandro Stefanazzi, Gustavo Cancelo, Juan Estrada and Eduardo Paolini

Low Threshold Acquisition Controller for Skipper Charge Coupled Devices, Guillermo Fernandez Moroni, Fernando Chierchie, Angel Soto, Miguel Sofo Haro, Leandro Stefanazzi, Juan Estrada, Gustavo Cancelo, Javier Tiffenberg, Ken Treptow, Neal Wilcer, Ted Zmuda and Eduardo E. Paolini

All-Digital High-Resolution PWM With a Wide Duty-Cycle Range, Juan Ignacio Morales, Fernando Chierchie, Pablo Mandolesi and Eduardo Paolini

Parallelism Analysis for a Multi-core Speech Recognition Architecture, Alejandro Pasciaroni, Pedro Julian and Andreas Andreou

SESSION F - Friday 15 - 13:30 to 14:30

Design of discrete-time current controllers for induction motor drives based on an individual channels analysis approach, Luis Esteban Venghi, Facundo Aguilera, Pablo Martín de la Barrera and Cristian Hernán De Angelo

Power converter topology for conditioning a fuel cells battery voltage, Adrian Gonnet, Sebastián Gómez Jorge, Claudio Busada and Jorge Solsona

CAE SPHD FORUM POSTERS

Boron-Induced Thermal Neutron Degradation of CMOS Image Sensors.
Fabricio Alcalde Bessia, Martín Pérez, Miguel Sofo Haro, Iván Sidelnik, Jeronimo Blostein, Sergio Suarez, Pablo Pérez, Mariano Gómez Berisso and José Lipovetzky.

Design, modeling, simulation and implementation of emerging techniques of game theory in serious games. Esteban Aitor Zapirain.

Detection and Identification of Alpha Particles in Real Time for a Future 222Rn Sensor. Clara Lucía Galimberti, Fabricio Alcalde Bessia, Mariano Gómez Berisso and Jose Lipovetzky.

Galvanically Isolated Data Transmission System using Capacitive Coupling. Luciano Janeiro, Martín Carrá and Mariano Garcia Inza.

Development of CMOS Sensors for Electron Microscopy. Horacio Mateos, Ivan Peric and Andrea Ferretti.

SLAM 2D con un Vehículo Autónomo de Superficie. Leonardo Garberoglio.

First steps in the design and development of Fine Sun Sensors for space applications in Argentina. Martha Díaz Salazar, Nadia Kondratiuk, Analía Moreno, Mónica Martínez Bogado and Mariana Tamasi.

Environmental conditions remote monitoring for health care. Camilo García, Alexis Sparapani and Sergio Ponce.

Modeling and simulation of memristive devices using Verilog-A. Fabrizio Di Francesco, Natalia Caroli, Mariano Barella, Gabriel Andrés Sanca and Federico Golmar.

Neutron Radiography Based on COTS CMOS Image Sensors. Martín Pérez, José Lipovetzky, Fabricio Alcalde Bessia, Miguel Sofo Haro, Julio Marin, Fernando Sánchez, Mariano Gómez Berisso and Juan Jerónimo Blostein.

Integrated INS/GNSS Navigation System Analysis. Elián Hanisch and Javier García.

Electrical Characterization of HfO₂ ReRAM Memories. First results. Fabiola de Las Casas Escardo, Francesca Campabadal and Adrian Faigón.

Development and Test of an USB Interface to Use in Applications Based on FPGA. Edwin Barragan, Martín Pérez, José Lipovetzky, Fabricio Alcalde Bessia and Cristian Sisterna.

Simulation based strategy for addressing CMOS degradation at the design stage. Fernando L. Aguirre, Sebastián M. Pazos, Andrés Fontana, Félix Palumbo, Pedro Julián.

Simplicial Neural Networks. Nicolás Rodríguez, Pedro Julián, Eduardo Paolini.